

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IPW

Applicant: Daniel A. Day

Title: Selective Control of Test-Access Ports in Integrated Circuits

Docket No.: 884.879US1

Filed: June 30, 2003

Examiner: Unknown



Serial No.: 10/612293

Due Date: N/A

Group Art Unit: 2857

**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

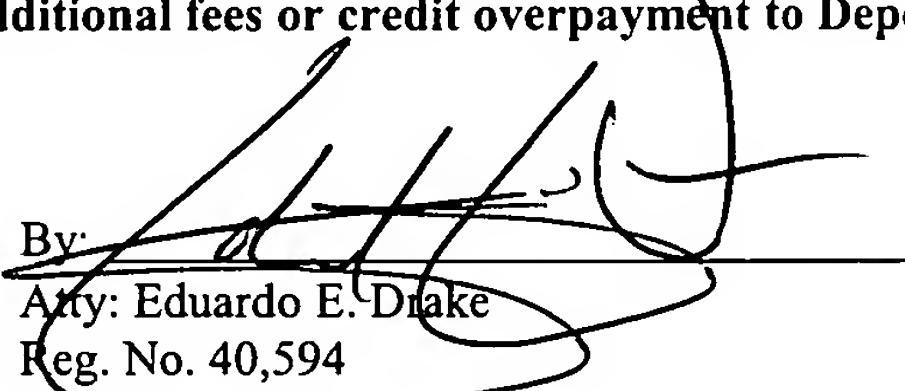
X A return postcard.

X A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 11 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By:   
Atty: Eduardo E. Drake  
Reg. No. 40,594

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 21 day of May, 2004.

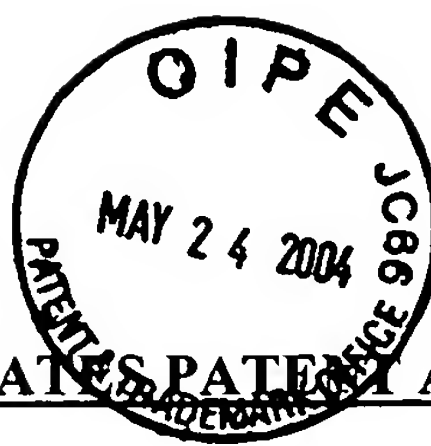
KACIA LEE  
Name

Kacia Lee  
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

S/N 10/612293



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Daniel A. Day	Examiner:	Unknown
Serial No.:	10/612293	Group Art Unit:	2857
Filed:	June 30, 2003	Docket:	884.879US1
Title:	Selective Control of Test-Access Ports in Integrated Circuits		
Assignee:	Intel Corporation	Customer No:	21186

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No : 10/612293

Filing Date: June 30, 2003

Title: Selective Control of Test-Access Ports in Integrated Circuits

Assignee: Intel Corporation

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Dkt: 884.879US1 (INTEL)

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

DANIEL A. DAY

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Attorneys for Intel Corporation

P.O. Box 2938

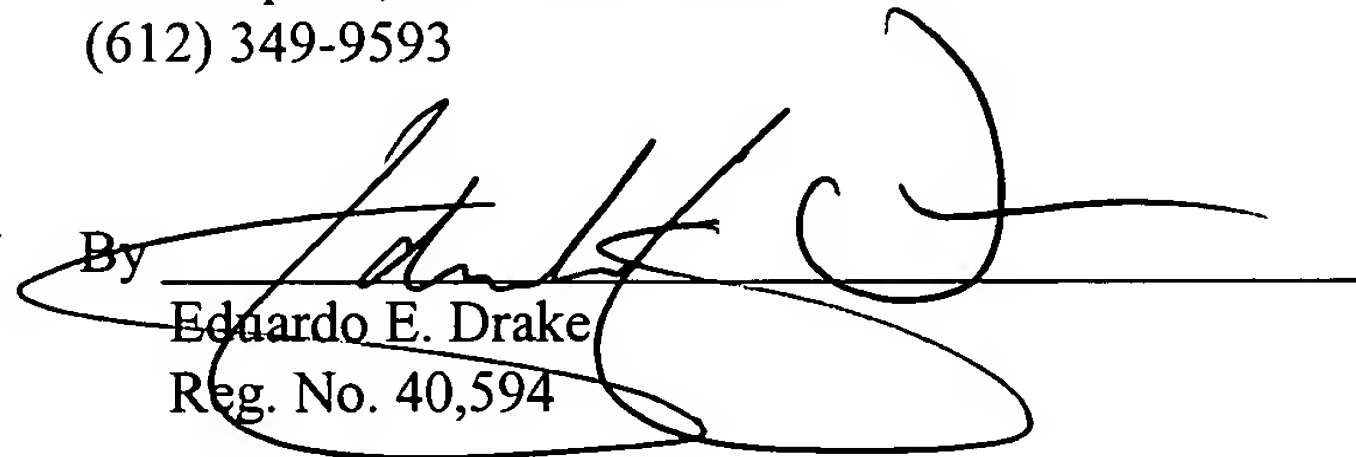
Minneapolis, Minnesota 55402

(612) 349-9593

Date

21 May 2004

By

  
Eduardo E. Drake  
Reg. No. 40,594

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Name

KACIA UEE

Signature

Kacia Lee

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	10/612293
Filing Date	June 30, 2003
First Named Inventor	Day, Daniel
Group Art Unit	2857
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 884.879US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,434,804	07/18/1995	Bock, Robert , et al.	364	579	12/29/1993
	US-5,583,430	12/10/1996	Dinteman, Bryan J.	324	158.1	04/19/1994
	US-5,708,773	01/13/1998	Jeppesen, III, James H., et al.	395	183.06	07/20/1995
	US-6,052,811	04/18/2000	Ahsuri, Roni	714	745	04/15/1997
	US-6,101,622	08/08/2000	Lesmeister, Gary J.	714	724	04/27/1998
	US-6,181,151	01/30/2001	Wasson, Will	324	765	10/28/1998
	US-6,205,560	03/20/2001	Hervin, Mark W.	714	34	02/27/1996
	US-6,266,793	07/24/2001	Mozdzen, Thomas J., et al.	714	727	02/26/1999
	US-6,380,730	04/30/2002	Arkin, Brian J., et al.	324	158.1	07/12/2000
	US-6,392,404	05/21/2002	Kuglin, Philip T.	324	158.1	07/31/2000
	US-6,392,866	05/21/2002	Dinteman, Bryan J.	361	206	04/18/2000

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached